



Substitute for form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet 1 of 3

Complete if Known

Application Number	10/815,604
Filing Date	03/31/2004
First Inventor	HO, Andrew
Art Unit	2611
Examiner	Perilla, Jason
Atty Docket #	
Rambus #	RA290.CIP1.US

US Patent Documents

US Patent Documents						
Examiner Initials*	Cite No.	Document No.	Date (MM-DD-YYYY)	Patentee	Class	Subclass
	1 0000119	6055297	04/25/2000	Terry, John Brian	379	1
	2 0000192	5802073	09/01/1998	Platt, Alfred	371	22.5
	3 0000328	5732089	03/24/1998	Negi, Keiji	371	5.1
	4 0000375	2002-073373	06/13/2002	Nakao, Michinobu	714	738
	5 0000460	6230022	05/08/2001	Sakoda, Kazuyuki	455	522
	6 0000471	5228042	07/13/1993	Gauthier et al.	371	20.05
	7 0000592	5369755	11/29/1994	Berkovich, Semyon	375	500
	8 0000606	6674998	01/06/2004	Prentice	455	114.2
	9 0000653	5917856	06/29/1999	Torsti, Simo-Pekka	375	231
	10 0000663	6631486	10/07/2003	Komatsu, Yoshihide	714	724
	11 0000886	5761216	06/02/1998	Sotome, Tetsuo	371	27.1
	12 0001052	6650698	11/18/2003	Liau, Thomas C.	375	229
	13 0001060	5483676	01/09/1996	Mahany, Ronald L.	455	67.4
	14 0001081	6671847	12/30/2003	Chao et al.	714	744
	15 0001227	6378079	04/23/2002	Mullarkey	713	401
	16 0001362	6160790	12/12/2000	Bremer, Gordon	310	201
	17 0001406	6339387	01/15/2002	Koga, Makoto	341	100
	18 0001792	2001-0016929	08/23/2001	Bonneau, Dominique P.	714	735
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	21 0001964	6606041	08/12/2003	Johnson, Brian	341	120
	22 0001965	6003118	12/14/1999	Chen	711	167
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	24 0002315	6421801	07/16/2002	Maddux, John T.	714	744
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	27 0002579	2001-0021987	09/13/2001	Govindarajan et al.	714	705
	28 0002645	6331787	12/18/2001	Whitworth.	326	30

Examiner Signature:

/J.P./

Date Considered:

05/12/2008

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Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT Sheet 2 of 3	Complete if Known	
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	0002852 29	6222380	04/24/2001	Gerowitz et al.	326	38
	0002996 30	5761212	06/02/1998	Foland, Jr.; William R.	371	21.2
	0003024 31	6154659	11/28/2000	Jalali, Ahmad	455	522
	0003093 32	5197062	03/23/1993	Picklesimer; David D.	370	13
	0003193 33	5999022	12/07/1999	Iwata, Toru	327	112
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	0006305 37	6295152	09/25/2001	Wedding; Berthold	398	209
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Foreign Patent Documents								
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	0003556	EP 40	EP1134668A2	09/19/2001		G06F 13	40	
	0006306	WO 41	02093821	11/21/2002	Koninklijke Philips Elect	H04L	1/24	
	0006307	WO 42	00/27065	05/11/2000	Broadcom Corp	H04L	1/24	
	0006408	EP 43	1 127 423 A0	08/29/2001	Broadcom Corporation	H04L	1/24	

Non-Patent Literature Documents			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T**
	0003614 44	GRANBERG, TOM, "Handbook of Digital Techniquis for High-Speed Design." Prentice Hall Modern Semiconductor Design Series. Copyright 2004 by Pearson Education, Inc. 12 pages.	
	0003620 45	MAXFIELD, C., "The Ouroboros of the Digital Consciousness: Linear-Feedback-Shift Registers," EDN, pp. 135-142, January 4, 1996	
	0003663 46	DALLY ET AL., "Multi-gigabit Signaling with CMOS," DARPA funded 1997	

Examiner Signature:	/J.P./	Date Considered:	05/12/2008
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	46 Cont.	presentation, 22 pages, May 12, 1997.	
	0003901 47	COVA ET AL., "Characterization of Individual Weights in Transversal Filters and Application to CCD's, IEEE Journal of Solid-State Circuits, Vol. SC-17, No. 6, December 1982, pp. 1054-1061.	
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	0003939 49	SOUMYANATH, K. et al, "Accurate On-Chip Interconnect Evaluation: A Time-Domain Technique." IEEE Journal of Solid-State Circuits, Vol. 34, No. 5, May 1999, pp. 623-631	
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	0004714 53	HOROWITZ, MARK, "Lecture 15: Transmitter and Receiver Design." Computer Systems Laboratory - Stanford University. Copyright 2000. Pages 15-1 to 15-35.	
	0004739 54	CYPRESS SEMICONDUCTOR CORPORATION, "HOTLink™ Built-In Self-Test (BIST)." March 11, 1999. 13 pages.	
	0004804 55	PFAFF, A., "Test high-speed drivers with bursts and pseudorandom bit patterns," EDN, pp. 133-136, May 12, 1994 (3 pages)	
	0006308 56	ELLERMEYER et al., "A 10-Gb/s Eye-Opening Monitor IC for Decision-Guided Adaptation of the Frequency Response of an Optical Receiver." IEEE Journal of Solid-State Circuits, Vol 35, No.12, December 2000. Pages 1958-1963.	

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